

AMENDMENTS TO THE CLAIMS

1. (Currently Amended) A method of displaying an image, comprising:
identifying, by a video controller, a first updated portion of first video image data that has changed since a previous transmission to a first display device;

transmitting a the first updated portion of the first video image data from a the video controller, without transmitting a remaining portion of the first video image data, over a bus to a first video memory contained within a the first display device;

identifying, by the video controller, a second updated portion of second video image data that has changed since a previous transmission to a second display device; and

transmitting a the second updated portion of second video image data from the video controller, without transmitting a remaining portion of the second video image data, over the to a second video memory contained within a the second display device;

wherein the first updated portion and the second updated portion are transmitted over a shared communication channel coupled between the video controller, the first display device and the second display device.

updating the first video memory with the first portion; and

updating the second video memory with the second portion,

wherein the first portion contains video data representing a part of the first video image data that has changed since a previous transmission to the first display device, and excludes a substantial part of the first video image data that is unchanged since the previous transmission to the first display device;

wherein the second portion contains video data representing a part of the second video image data that has changed since a previous transmission to the second display device, and excludes a substantial part of the second video image data that is unchanged since the previous transmission to the second video display device, and

wherein at least one of said updating the first video memory and said updating the second video memory is repeated at irregular intervals.

2. (Currently Amended) The method of claim 1, further comprising refreshing a first displayed image in the first display device from the a first video memory of the first display device.

3. (Currently Amended) The method of claim 1, wherein transmitting a the first updated portion is repeated at regular intervals.

4. (Cancelled)

5. (Currently Amended) The method of claim 132, wherein updating the first video memory is repeated at irregular intervals; and wherein said irregular intervals are based on detecting a change in the first video image data since the previous transmission to the first display device.

6-9. (Cancelled)

10. (Previously Presented) The method of claim 1, wherein the first portion and the second portion are formatted differently.

11. (Previously Presented) The method of claim 1, wherein the first portion and the second portion are formatted alike.

12. (Previously Presented) The method of claim 1, wherein the first portion includes an address to identify the first video device and the second portion includes an address to identify the second video device.

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13. (Previously Presented) The method of claim 1, further comprising: transmitting a third portion of the first video image data to the first display-device; time-stamping the first and third portions before transmission; and synchronizing a presentation of the first and third portions based on the time-stamping.

14. (Cancelled)

15. (Previously Presented) The system of claim 22, wherein the first display device includes a protocol handler to interpret the first video data.

16. (Previously Presented) The system of claim 22, wherein the first display device includes a timing generator to generate timing signals for a display.

17. (Previously Presented) The system of claim 16, wherein the first display device includes a control circuit to configure the timing generator.

18. (Previously Presented) The system of claim 22, wherein the first display device includes a scalar circuit to change a granularity of video image.

19. (Previously Presented) The system of claim 18, wherein the first display device includes a control circuit to configure the scalar circuit.

20. (Previously Presented) The system of claim 22, wherein the first display device includes a display interface to at least one of a CRT and a flat panel.

21. (Previously Presented) The system of claim 22, wherein the first display device includes at least one of a CRT and a flat panel.

22. (Currently Amended) A system, comprising:

a shared communication channel;

~~a first display device coupled to the shared communication channel and having a first video memory contained within the first display device;~~

~~a second display device coupled to the shared communication channel and having a second video memory contained within the second display device; and~~

~~a bus coupled to the first video memory and the second video memory; and~~

~~a video controller coupled to the bus-shared communication channel to update transmit an identified, first updated portion of first video image data that has changed since a previous transmission to the first display device a first image displayed by the first display device by transmitting over the bus-shared communication channel to the first video memory display device, first video data that has changed since a previous update to the first image and by not transmitting a substantial portion of the first video data that has not changed since the previous update to the first image, and to update a second image displayed by the second display device by transmitting transmit an identified, second updated portion of second video image data that has changed since a previous transmission to the second display device over the bus-shared communication channel to the second video memory display device, second video data that has changed since a previous update to the second image and by not transmitting a substantial portion of the second video data that has not changed since the previous update to the second image;~~

~~wherein a protocol of the first video data is different than a protocol of the second video data.~~

23. (Cancelled)

24. (Currently Amended) The system of claim 22, wherein:

~~the first display device includes a first address decoder to decode a first device address associated with the first updated portion of first video image data received over the bus-shared communication channel; and~~

~~the second display device includes a second address decoder to decode a second device address associated with the second updated portion of second image video data received over the bus shared communication channel.~~

25. (Cancelled)

26. (Currently Amended) The system of claim 24, further comprising a non-display device coupled to the ~~bus~~ shared communication channel to receive non-video data.

27. (Original) The system of claim 24, wherein the first and second address decoders each decode a broadcast address in a broadcast message to be processed by the first and second display devices.

Please add the following new claims:

-- 28. (New) The system of claim 22, wherein the shared communication channel comprises a bus.

29. (New) The system of claim 22, wherein the shared communication channel comprises a daisy chain.

30. (New) The system of claim 22, wherein the first display device comprises:
an interface coupled to the shared communication channel;
a video memory coupled to the interface, the interface to update the video memory if an address associated with an updated portion of video image data over the shared communication channel matches an address of the first display device; and
a control circuit to refresh a displayed image in the first display from the first video memory.

31. (New) The system of claim 22, wherein the second display device comprises:
an interface coupled to the shared communication channel;
a video memory coupled to the interface, the interface to update the video memory of the second display device if an address associated with an updated portion of video image data over the shared communication channel matches a display device address; and,
a control circuit to refresh a displayed image in the second display device from the video memory.

32. (New) The method of claim 1, further comprising:
updating a video memory of the first display device if an address associated with an updated portion of video image data received over the shared communication channel matches a first display device address.

33. (New) The method of claim 1, further comprising:

updating a video memory of the second display device if an address associated with an updated portion of video image data received over the shared communication channel matches a second display device address.

34. (New) A method comprising:

detecting, by a display device, an updated portion of video image data received over a shared communication channel;

updating a video memory of the display device if an address associated with the updated portion of the video image data matches a display device address; and

refreshing a displayed image in the display device from the video memory.

35. (New) The method of claim 34, wherein the updated portion of video image data represents video image data that has changed since a previous transmission to the display device and excludes a substantial portion of the video image data that is unchanged since the previous transmission to the display device.

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36. (New) The method of claim 34, wherein prior to detecting the updated portion of video image data, the method comprises:

receiving the display device address assigned to the display device during display device initialization.

37. (New) The method of claim 34, wherein updating the video memory of the display device is repeated at regular intervals.

38. (New) The method of claim 34, wherein updating the video memory of the display device is repeated at irregular intervals.

39. (New) A display device comprising:

a video memory;

an interface coupled to the video memory, the interface to detect an updated portion of video image data received over a shared communication channel and to update the video memory if an address associated with the updated portion of video image data matches a display device address; and

a control circuit to refresh a display image in the displayed device from the video memory.

40. (New) The display device of claim 39, wherein the interface is to receive the display device address assigned to the display device during display device initialization.

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41. (New) The display device of claim 39, wherein the display device includes one of a CRT and a flat panel.

42. (New) The display device of claim 39, wherein the shared communication channel comprises one of a bus and a daisy chain.

43. (New) The display device of claim 39, wherein the updated portion of video image data represents video image data that has changed since a previous transmission to the display device and excludes a substantial portion of the video image data that is unchanged since the previous transmission to the display device. --
